

Memory Product Specification

SDRAM PC133 Unbuffered DIMM

Description

The Module is SDRAM PC133 CL2/3 Unbuffered Memory module. The Module density from 64MB to 512MB, it consists 8/16/32MX8 bit SDRAM 133 Synchronous DRAMs in TSOP II packages, Memory Module intended for mounting into 168-pin edge connector sockets. The electrical and mechanical specifications are as follows

Features

- JEDEC Standard
- SDRAM Speed Grade : 133Mbps
- Unbuffered DIMM : 168-pin
- Memory Organization : x8 TSOP II DRAM chip
- SDRAM interface : SSTL_3
- CAS latency : 2-3-3
- Bandwidth : 1060MB/s
- VDD voltage : 3.3±0.3V
- VDDQ voltage : 3.3±0.3V
- Serial presence detect with EEPROM.
- PCB height : **1.25 inch**
- RoHS Compliant
- Application : Desktop